

IN THE CLAIMS:

Please amend claims 3, 7, 8, 11, 13 and 18 as follows:

C1  
Sub E2

3. (Twice Amended) A circuit according to claim 1 wherein said subtractor circuit further comprises a halving circuit which [reduces a signal by one-half its] inputs a signal having an input amplitude and outputs the signal at one-half the input amplitude.

C2  
Sub D1

7. (Twice Amended) A circuit comprising:

- a first circuit having a first input and a first output, wherein said first output includes a function of a signal at said first input and also includes a noise component resulting from noise experienced by said first circuit;
- a second circuit having a second input and a second output;
- a signal supplying circuit [supplying] supplying to the second input a signal an inverse of the signal at the first input; and
- a third circuit having a third input connected to said first output and a fourth input connected to said second output, and subtracting said second output from said first output.

cont  
C2  
13~~8~~ (Twice Amended) A circuit according to claim 12~~7~~ wherein said third circuit further comprises a halving circuit which [reduces a signal by one-half its] inputs a signal having an input amplitude and outputs the signal at one-half the input amplitude.

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17  
11. (Three Times Amended) An integrated circuit chip (IC) comprising:  
a plurality of analog circuits, each proximal to [to] each other, and each of said plurality of analog circuits producing an output signal which includes a function of an input signal and also includes a noise component resulting from noise experienced by said plurality of analog circuits;

3  
C  
a noise separator circuit, proximal to said plurality of analog circuits, and producing a noise signal based on noise experienced by said noise separator circuit, wherein the noise signal is approximately equal to the noise component of the output signal output by each of the plurality of analog circuits; and

a noise canceling circuit which processes said output signals with said noise signal to reduce the noise component of the output signal output by each of the plurality of analog circuits.

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18. (Twice Amended) An IC according to claim 12~~11~~ wherein said noise canceling circuit further comprises a halving circuit which [reduces a signal by one-half its] inputs a signal having an input amplitude and outputs the signal at one-half the input amplitude.

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